

01.

- 1.1.
- 1.2.
- 1.3.
- 1.4.

1.1.

, CPU .
ACL.

1.2.

1. (CPU) ;
2. ;
3. ;
4. - ACL.

1. :

monitor session <session> source {interface <interface-list> cpu} {rx tx both} no monitor session <session> source {interface <interface-list> cpu} !	CPU <session>. {rx tx both} . no <session>.

2. (CPU) :

monitor session <session> destination interface <interface-number> no monitor session <session> destination interface <interface-number> !	<session>. no <session>.

3. :

monitor session <session> sample rate <num> no monitor session <session> sample rate !	<0-65535> <session>. no - .

4. - ACL:

monitor session <session> source {interface <interface-list>} access-group <num> {rx} no monitor session <session> source {interface <interface-list>} access-group <num> !	<session>. access-group <num> rx ACL . no <session>.

1.3.

1/0/1 :

1. 1/0/7 (ingress);
2. 1/0/9 (egress);
3. CPU ;
4. TCP 1.2.3.4/24, 5.6.7.8/24 (ingress) 1/0/5.

:

```
Switch(config)#monitor session 1 destination interface ethernet 1/0/1
Switch(config)#monitor session 1 source interface ethernet 1/0/7 rx
Switch(config)#monitor session 1 source interface ethernet 1/0/9 tx
Switch(config)#monitor session 1 source cpu
Switch(config)#access-list 120 permit tcp 1.2.3.4 0.0.0.255 5.6.7.8 0.0.0.255
Switch(config)#monitor session 1 source interface ethernet 1/0/15 access-list 120 rx
```

1.4.

- **Port-channel** ;
- , ;
- **ACL ingress** ;
- . ,. [02. RSPAN](#).